

## 20V N-Channel MOSFETs

## **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max.	I <sub>D</sub>
20V	$30$ m $\Omega$	5.5A

#### **Features**

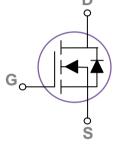
- 20V, 4.5A,  $R_{DS(ON)}Max.=30m\Omega@V_{GS} = 4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 1.8V Gate Drive Applications

## **Applications**

- Notebook
- Load Switch
- Hend-Held Instruments







## Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	20	V	
$V_{GS}$	Gate-Source Voltage	±10	V	
I <sub>D</sub>	Drain Current – Continuous (T <sub>C</sub> =25°C)	5.5	А	
	Drain Current – Continuous (T <sub>C</sub> =100°C)	3.9	А	
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	22	А	
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25°C)	1.56	W	
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

Note 1: Exceed these limits to damage to the device.

Note 2: Exposure to absolute maximum rating conditions may affect device reliability.



## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage V <sub>GS</sub> =0V , I <sub>D</sub> =250uA		20			V
	Drain Course Leakage Current	$V_{DS}$ =20V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			1	uA
IDSS	Drain-Source Leakage Current	$V_{DS}$ =16V , $V_{GS}$ =0V , $T_J$ =85 $^{\circ}$ C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±10V , V <sub>DS</sub> =0V			±100	nA

#### On Characteristics

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =4.5A		23	30	mΩ
		$V_{GS}$ =2.5 $V$ , $I_D$ =3.5 $A$		28	40	1115.2
$V_{GS(th)}$	Gate Threshold Voltage	V -V 1 -250uA	0.5	0.6	1.2	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-2		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =4.5A		30		S

## **Dynamic and switching Characteristics**

$Q_g$	Total Gate Charge <sup>2,3</sup>		7.7	
$Q_gs$	Gate-Source Charge <sup>2,3</sup>	$V_{DS}$ =10V , $V_{GS}$ =4.5V , $I_{D}$ =4A	0.9	nC
$Q_gd$	Gate-Drain Charge <sup>2,3</sup>		2.4	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>		4.1	
T <sub>r</sub>	Rise Time <sup>2,3</sup>	$V_{DD}$ =10V , $V_{GS}$ =4.5V , $R_{G}$ =25 $\Omega$	11.6	nS
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>	I <sub>D</sub> =1A	23.9	113
T <sub>f</sub>	Fall Time <sup>2,3</sup>		7.6	
C <sub>iss</sub>	Input Capacitance		535	
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =10V , V <sub>GS</sub> =0V , F=1MHz	60	pF
$C_{rss}$	Reverse Transfer Capacitance		34	

### **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			5.5	Α
I <sub>SM</sub>	Pulsed Source Current	V <sub>G</sub> =V <sub>D</sub> =UV , Force Current			11.4	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V

#### Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq 300$ us , duty cycle  $\leq 2\%$ .
- 3. Essentially independent of operating temperature.



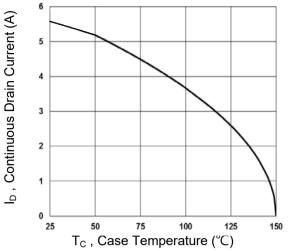


Fig.1 Continuous Drain Current vs. Tc

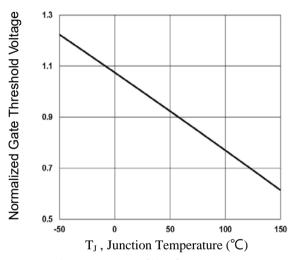


Fig.3 Normalized  $V_{th}$  vs.  $T_J$ 

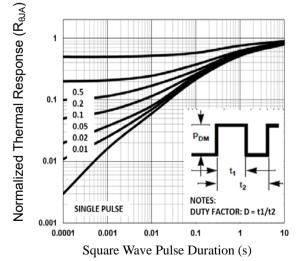


Fig.5 Normalized Transient Impedance

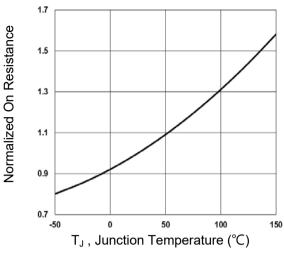


Fig.2 Normalized RDSON vs. T<sub>J</sub>

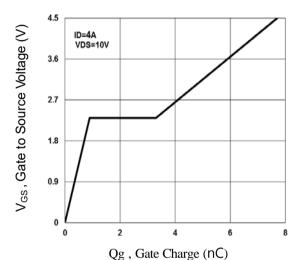


Fig.4 Gate Charge W aveform

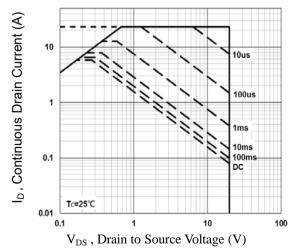
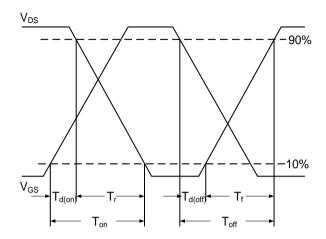


Fig.6 Maximum Safe Operation Area





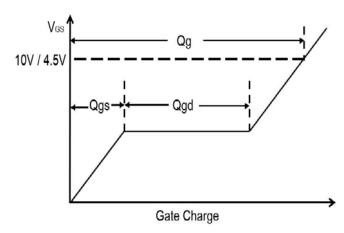
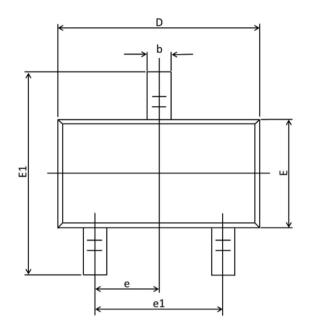


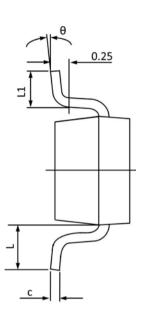
Fig.7 Switching Time Waveform

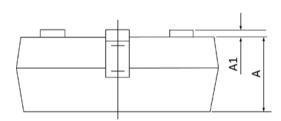
Fig.8 Gate Charge Waveform



# **SOT-23 PACKAGE INFORMATION**







Crombal	<b>Dimensions In Millimeters</b>		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	0.900	1.000	0.035	0.039	
A1	0.000	0.100	0.000	0.004	
b	0.300	0.500	0.012	0.020	
c	0.090	0.110	0.003	0.004	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
e	0.950 TYP.		0.037	TYP.	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF.		0.022	REF.	
L1	0.300	0.500	0.012	0.020	
θ	1°	7°	1°	<b>7</b> °	