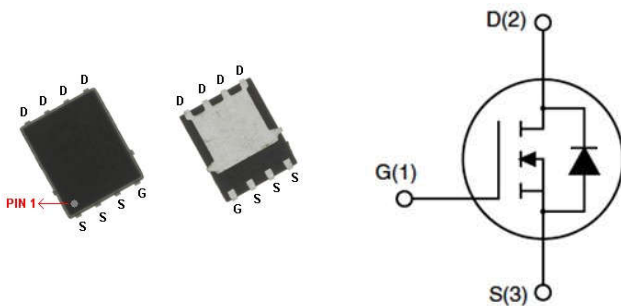


30V, 150A N-Channel MOSFET

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

PPAK5×6 Pin Configuration



Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max.	I_D
30 V	2.0 m Ω	150 A

Features

- 30 V, 150 A, $R_{DS(ON)}$ Max. = 2.0 m Ω @ $V_{GS} = 10$ V
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Networking
- LED Lighting Applications
- Quick Charger Applications
- DC-DC Power Management

Absolute Maximum Ratings ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_C = 25^\circ\text{C}$)	150	A
	Drain Current – Continuous ($T_C = 100^\circ\text{C}$)	75	A
I_{DM}	Drain Current – Pulsed ¹	430	A
E_{AS}	Single Pulse Avalanche Energy ²	180	mJ
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	35	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.5	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	85	
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ\text{C}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}$, $I_D = 250\ \mu\text{A}$	30			V
I_{DSS}	Drain-Source Leakage Current	$V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 25^\circ\text{C}$			1	μA
		$V_{DS} = 24\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 125^\circ\text{C}$			10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 20\text{ V}$, $V_{DS} = 0\text{ V}$			± 100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}$, $I_D = 30\text{ A}$		1.55	2.0	$\text{m}\Omega$
		$V_{GS} = 4.5\text{ V}$, $I_D = 15\text{ A}$		2.0	3.0	$\text{m}\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\ \mu\text{A}$	1.0	1.6	2.5	V
gfs	Forward Transconductance	$V_{DS} = 5\text{ V}$, $I_D = 15\text{ A}$		48		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q_g	Total Gate Charge	$V_{DS} = 15\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 24\text{ A}$		40		nC
Q_{gs}	Gate-Source Charge			6		
Q_{gd}	Gate-Drain Charge			19		
$T_{d(on)}$	Turn-On Delay Time	$V_{DS} = 15\text{ V}$, $V_{GS} = 10\text{ V}$, $R_{GEN} = 1\ \Omega$, $I_D = 1\text{ A}$		20		ns
T_r	Rise Time			32		
$T_{d(off)}$	Turn-Off Delay Time			75		
T_f	Fall Time			28		
C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}$, $V_{GS} = 0\text{ V}$, $F = 1\text{ MHz}$		4800		pF
C_{oss}	Output Capacitance			735		
C_{rss}	Reverse Transfer Capacitance			420		

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G = V_D = 0\text{ V}$, Force Current			150	A
I_{SM}	Pulsed Source Current				600	A
V_{SD}	Diode Forward Voltage	$V_{GS} = 0\text{ V}$, $I_S = 30\text{ A}$			1.2	V
T_{rr}	Body Diode Reverse Recovery Time	$I_S = 1\text{ A}$, $dI/dt = 100\text{ A}/\mu\text{s}$		49	85	ns
Q_{rr}	Body Diode Reverse Recovery Charge				18	35

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. E_{AS} condition: $T_J = 25^\circ\text{C}$, $V_{DD} = 25\text{ V}$, $V_{GS} = 10\text{ V}$, $L = 0.1\text{ mH}$, $I_{AS} = 60\text{ A}$, $R_G = 25\ \Omega$
3. The data tested by pulsed , pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.

Typical Performance Characteristics

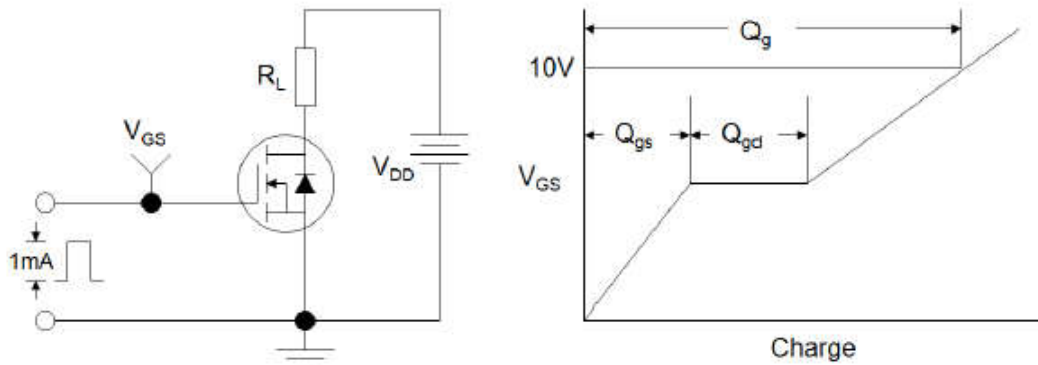


Figure1: Gate Charge Test Circuit & Waveform

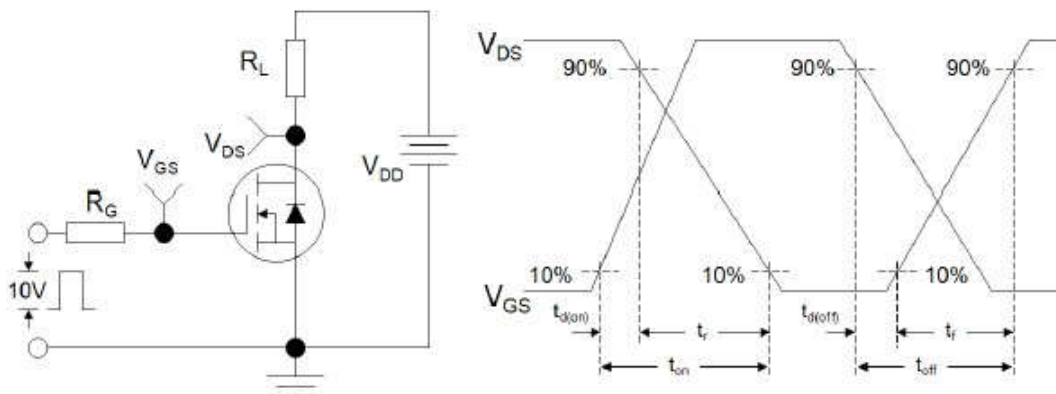


Figure 2: Resistive Switching Test Circuit & Waveforms

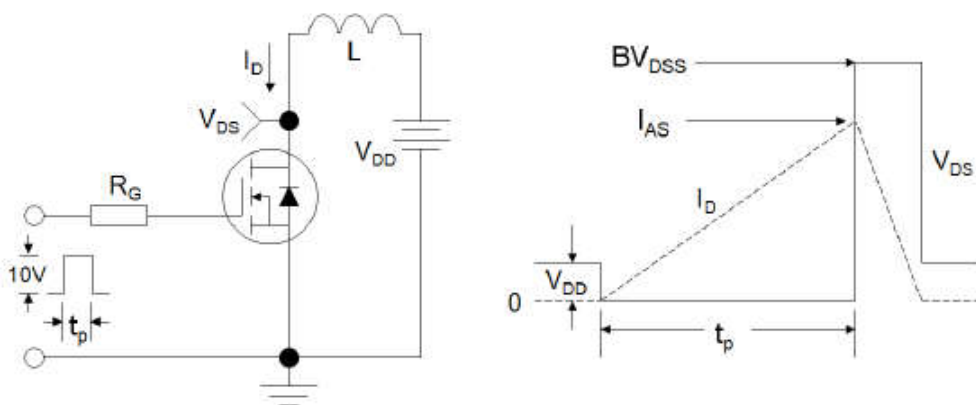


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

Typical Performance Characteristics (Continued)

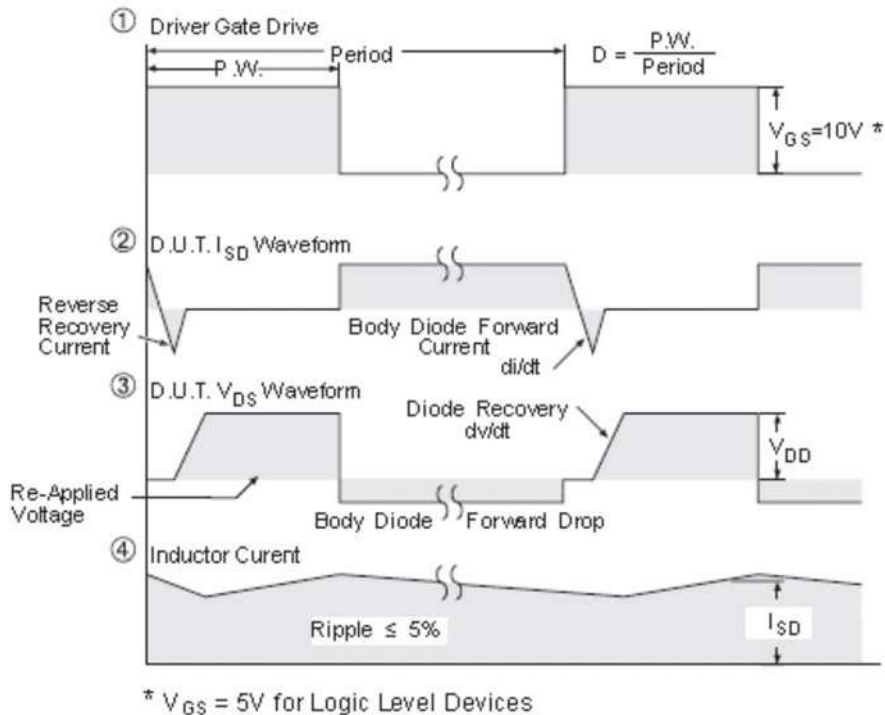
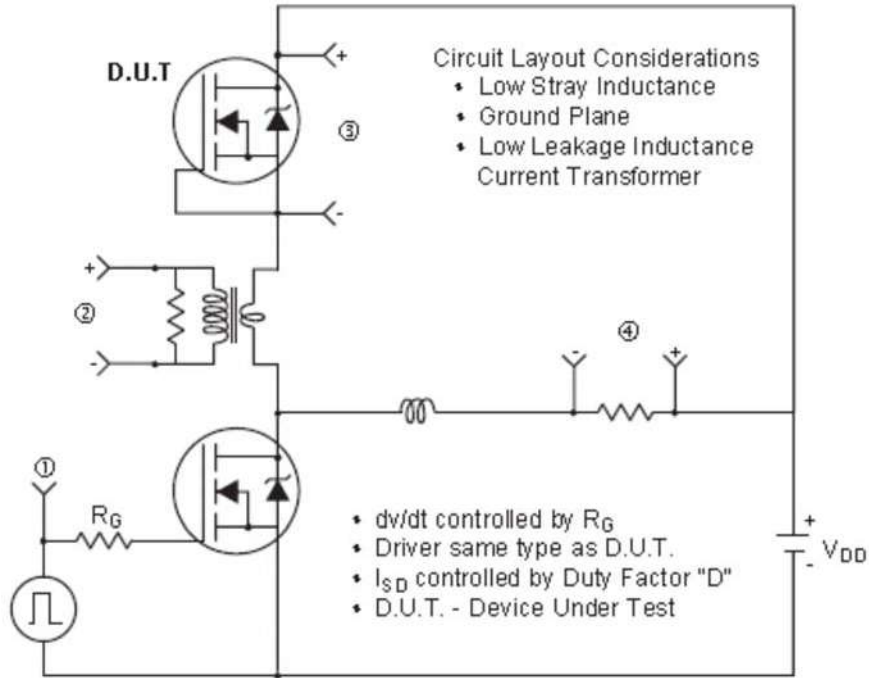
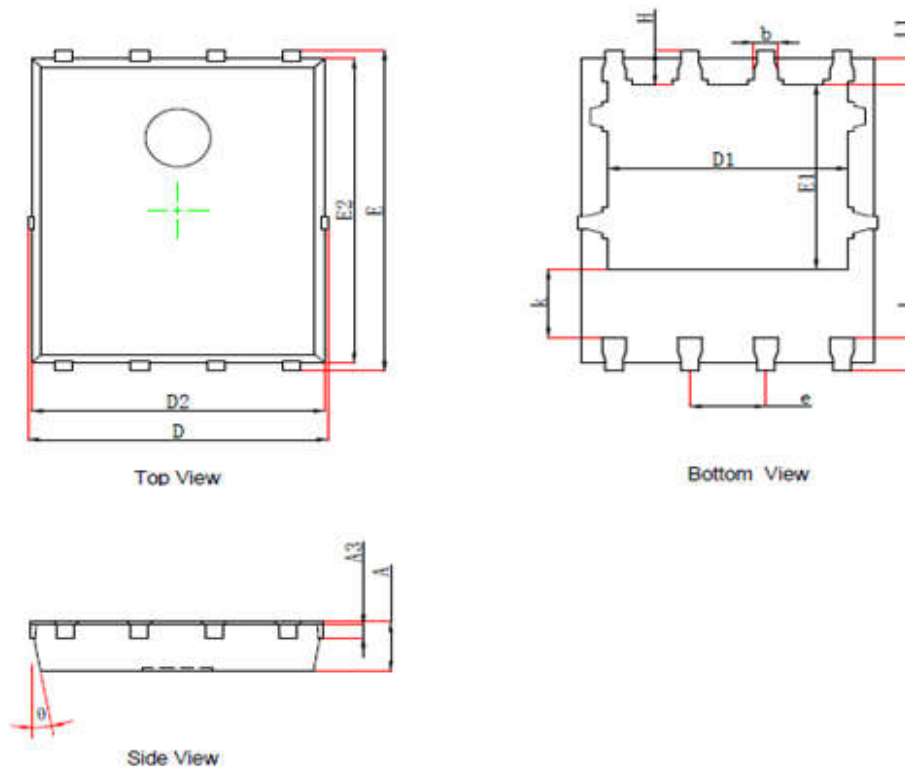


Figure 4: Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)

PPAK5×6 Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.000	0.035	0.039
A3	0.254REF		0.010REF	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270BSC		0.050BSC	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

V 1.0