SUMSEMI

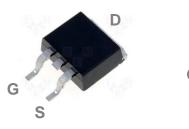
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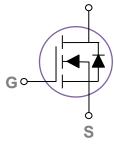
80V N-Channel MOSFETs

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO-263 Pin Configuration





n

BV_{DSS}	R _{DS(ON)} Max.	I _D
80V	$9.5 m\Omega$	80A

Features

- 80V,80A, $R_{DS(ON)}Max. = 9.5m\Omega @V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	80	V
V _{GS}	Gate-Source Voltage	±25	V
1_	Drain Current – Continuous (Tc=25°C)	80	A
ID	Drain Current – Continuous (T _C =100°C)	55	А
IDM	Drain Current – Pulsed ¹	300	А
E _{AS}	Single Pulse Avalanche Energy ²	210	mJ
I _{AS}	Single Pulse Avalanche Current ²	64	А
P _D	Power Dissipation (Tc=25°C)	170	W
T _{STG}	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 150	°C

Note 1: Exceed these limits to damage to the device.

Note 2: Exposure to absolute maximum rating conditions may affect device reliability.



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	80			V
IDSS	Drain-Source Leakage Current	V _{DS} =100V , V _{GS} =0V , T _J =25°C			1	uA
		V _{DS} =80V , V _{GS} =0V , T _J =85°C			10	uA
lgss	Gate-Source Leakage Current	V _{GS=} 20V , V _{DS} =0V			100	nA

On Characteristics

Rds(on)	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =20A		8.3	9.5	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	-V _{GS} =V _{DS} , I _D =250uA	2	3	4	V
	V _{GS(th)} Temperature Coefficient			-8		mV/℃
gfs	Forward Transconductance	V _{DS} =5V , I _D =20A		65		S

Dynamic and switching Characteristics

Qg	Total Gate Charge ^{3,4}		70	
Q _{gs}	Gate-Source Charge ^{3,4}	V_{DS} =80V , V_{GS} =10V , I_{D} =10A	13	nC
Q_{gd}	Gate-Drain Charge ^{3 , 4}		15.2	
T _{d(on)}	Turn-On Delay Time ^{3 , 4}		22	
Tr	Rise Time ^{3 , 4}	V_{DD} =50V , V_{GS} =10V , R_G =6 Ω	16	n 0
T _{d(off)}	Turn-Off Delay Time ^{3 , 4}	I _D =1A	40	ns
T _f	Fall Time ^{3 , 4}		18	
Ciss	Input Capacitance		2800	
Coss	Output Capacitance	V_{DS} =25V , V_{GS} =0V , F=1MHz	200	pF
C _{rss}	Reverse Transfer Capacitance		75	
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	1.5	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	$V_G=V_D=0V$, Force Current			80	А
lsм	Pulsed Source Current				160	А
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A , TJ=25°C			1	V

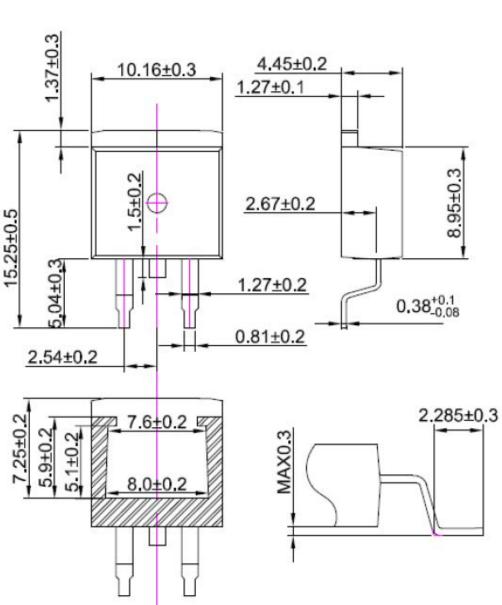
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.

2. $V_{DD}=25V, V_{GS}=10V, L=0.1 \text{mH}, I_{AS}=87 \text{A}., R_G=25\Omega$, Starting T_J=25°C.

3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.

4. Essentially independent of operating temperature.



TO-263 PACKAGE INFORMATION

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